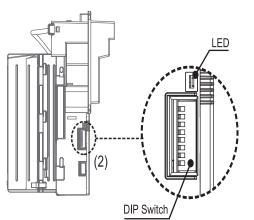
# XBA-AMD2 Option Switch Settings Supported bill AMD 1000, 2000 2bills.

	FUNCTION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
	Reject AMD 1000	ON							
*	Accept AMD 1000	OFF							
	Reject AMD 2000		ON						
*	Accept AMD 2000		OFF						
	Reserved			ON					
*	Reserved			OFF					
	Reserved				ON				
*	Reserved				OFF				
	Reserved					ON			
*	Reserved					OFF			
	Disable Bill Reject 4 Times BA Stop By 30 Sec						ON		
*	Enable Bill Reject 4 Times BA Stop By 30 Sec						OFF		
	Stack Banknote when Power-up							ON	
*	Reject Banknote when Power-up							OFF	
	Reserved								ON
*	Reserved								OFF

This dip switch is located at the side of XBA.



## **Currency Assign Data**

Interface Bill value	JPSTD	ICT104U	ccTalk	Pulse	RS232 A0
BV1	AMD 1000				
BV2		AMD 2000	AMD 2000	AMD 2000	AMD 2000

### RS232 A0 INFORMATION

Bi <b>ll</b> Type Data						
Code	Denomination					
1	Reserved					
2	Reserved					
3	AMD 1000					
4	AMD 2000					
5	Reserved					
6	Reserved					
7	Reserved					
8	Reserved					

Appendix

# XBA-AMD2(Pulse/JPSTD/ccTalk/ICT104U/RS232 A0)

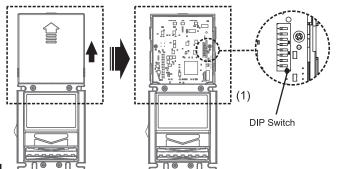
	FUNC	TION	SW1	SW2	SW3	SW4	SW5	SW6	SW7	SW8
	Pulse Mode	Other Mode								
	Inhibit Active HIGH		ON							
*	Inhibit Active LOW		OFF							
	Reserved			ON						
*	Reserved			OFF						
*	50ms LO / 50ms HI				OFF	OFF				
	50ms LO / 100ms HI				OFF	ON				
	65ms LO / 75ms HI				ON	OFF				
	100ms LO / 150ms HI				ON	ON				
*	Credit-Pulse Normal HIGH	ccTalk 16-bit Encryption mode & Restore Mode (See Table 1)					ON			
	Credit-Pulse Normal LOW	ccTalk 16-bit Decrypt mode & 8-bit checksum.(See Table.1)					OFF			
*	Pulse Mode							ON		
	Other Mode							OFF		
*	1 pulse / AMD 1000	ccTalk Mode							OFF	OFF
	2 pulses / AMD 1000	RS232 A0 Mode							OFF	ON
	4 pulses / AMD 1000	ICT104U Mode							ON	OFF
	20 pulses / AMD 1000	JPSTD Mode							ON	ON

#### ★ Manufacture setting

This dip switch is located on the CPU board, remove the CPU board cover first.

## Table 1

	SW1	SW5
8-Bit Checksum	OFF	OFF
Restore mode(Note.1)	OFF	ON
CRC 16-Bit Decrypt Checksum	ON	OFF
CRC 16-Bit Encryption Checksum	ON	ON



\*After setting dip switch of the credit pulses, you should reset the bill acceptor again.

Note: 1.Turn this dip to with restart the bill acceptor will restore. ccTalk address to its default value of 40 and the encryption key to its default value of 123456.

2.Calibration card is needed.

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